

ABSTRACT OF THE DISCLOSURE

To provide a large-scale integrated-circuit apparatus having a CPU and a plurality of circuit blocks to be operated in accordance with clock signals, which includes the circuit blocks for receiving reset signals to perform initialization and outputting reset completion signals and a CPU for logic-operating the initialization completion signals output from the circuit blocks and outputting a signal for canceling resetting to the circuit blocks in accordance with the logic-operation result.

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FOUO "2065T660"